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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		09989762
	Filing Date		2001-11-19
	First Named Inventor	Kenneth Y. Ogami	
	Art Unit	2123	
	Examiner Name	Osborne, Luke R.	
	Attorney Docket Number	CYPR-CD01175M	

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	4	6592626		2003-07-15	Bauchot , et al.	
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2	C. Ebeling et al.; "VALIDATING VLSI CIRCUIT LAYOUT BY WIRELIST COMPARISON"; Sept. 1983; In proceedings of the IEEE International Conference on Computer Aided Design (ICCAD-83), pp. 172-173	<input type="checkbox"/>
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4	Ohlrich et al.; "SUB-GEMINI: IDENTIFYING SUBCIRCUITS USING A FAST SUBGRAPH ISOMORPHISM ALGORITHM"; June 1993; In proceedings of the 30th IEEE/ACM Design Automation Conference 7 pages	<input type="checkbox"/>
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